

What Is Claimed Is:

1. A method for monitoring the operating readiness of at least one memory element (18, 20) assigned to an electronic unit (10), where a supply voltage of the electronic unit (10) is monitored.
2. The method as recited in Claim 1, where the supply voltage is monitored by initially dividing it down.
3. The method as recited in Claim 2, where the divided-down supply voltage is cyclically measured with the aid of an analog-digital converter (14).
4. The method as recited in Claim 2, where the divided-down supply voltage is cyclically read in at a general input (22) of an electronic processing unit (12).
5. The method as recited in Claim 2, where the divided-down supply voltage is evaluated at an interrupt input (24) of an electronic processing unit (12).
6. The method as recited in Claim 1, where the supply voltage is input into a comparator (16), of which the output signal is evaluated for monitoring the supply voltage.
7. The method as recited in Claim 6, where the output signal of the comparator (16) is cyclically read in at a general input (22) of an electronic processing unit (12).
8. The method as recited in Claim 6, where the output signal of the comparator (16) is evaluated at an interrupt input (24) of an electronic processing unit (12).

9. The method as recited in one of Claims 1 through 8, where in addition to the supply voltage of the electronic unit (10), the operating voltage of the at least one memory element (18, 20) is monitored.

10. An electronic unit, which is operated by a supply voltage and assigned at least one memory element (18, 20), the electronic unit (10) being designed in such a manner, that monitoring of the supply voltage of the electronic unit (10) is provided for monitoring the operating readiness of the at least one memory element (18, 20).

11. The electronic unit as recited in Claim 10, which has an electronic processing unit (12).

12. The electronic unit as recited in Claim 10 or 11, where the at least one memory element (18, 20) is a flash-memory element (18, 20).

13. The electronic unit as recited in one of Claims 10 through 13, where an analog-digital converter (14) is provided for monitoring the supply voltage.

14. The electronic unit as recited in one of Claims 10 through 13, where a comparator (16) is provided for monitoring the supply voltage.

15. Computer program having program code means, in order to implement a method as recited in one of Claims 1 through 9, when the computer program is executed on a computer or an appropriate processing unit, in particular a processing unit (12) in an electronic unit (10) as recited in Claim 11.

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16. A computer-program product having program code means, which are stored on a computer-readable storage medium, in order to implement a method as recited in one of Claims 1 through 9, when the computer program is executed on a computer or an appropriate processing unit, in particular a processing unit (12) in an electronic unit (10) as recited in Claim 11.